



Silicon Quantum Information Processing 2019: Conference and School

11–12 September 2019, Hughes Hall, Cambridge, UK

This Silicon Quantum Information Processing Conference and School will be of interest to leading researchers from the silicon QIP and quantum computing communities.

The benefits of silicon as a host material for QIP have been made clear by recent demonstrations of long-lived, high-fidelity silicon qubits and multi-qubit gates. A scalable QIP architecture will also require long-range qubit coupling, robust and scalable fabrication processes, and schemes to correct errors and protect against decoherence.

This meeting will bring together leading researchers from the silicon QIP community in particular, and the quantum computing community in general, who are interested in meeting these challenges.

School on Quantum Computing

Prior to the conference on the 12th, we will host a one-day school on Quantum Computing on the 11th where we will discuss state-of-the-art advances in quantum computing platforms, enabling technology such as cryogenic CMOS and quantum error correction techniques.

This conference has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 688539.

Contributions for oral and poster presentations are invited.

Topics of interest include

- Silicon nanodevices and qubit implementations
- Qubits and nanodevices based on semiconductor–superconductor hybrids
- Circuit quantum electrodynamics interfaced with spins in semiconductors
- Scalable architectures and fabrication processes
- Improving immunity to environmental noise
- Machine control for quantum systems
- Cryogenic electronics for quantum computing

Abstracts of a maximum of 300 words should be submitted online by 15 August 2019:
www.iopconferences.org/iop/1351/abstracts

Key dates

Abstract submission deadline:	15 August 2019
Early registration deadline:	22 August 2019
Registration deadline:	2 September 2019

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